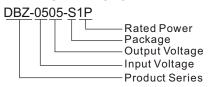


DBZ_S1P & DBZ_D1P Series

1W, FIXED INPUT, ISOLATED & UNREGULATED SINGLE OUTPUT DC-DC CONVERTER

PART NUMBER SYSTEM





PRODUCT FEATURES

- •Efficiency up to 82%
- Miniature SIP/DIP Package
- ●1500VDC Isolation
- Operating Temperature Range:
- -40°C ~ +105°C
- No External Component Required
- PCB Mounting
- Industry Standard Pinout
- continuous short circut protection

APPLICATIONS

The DBZ_S1P & DBZ_D1P Series are designed for application where isolated output is required from a distributed power system.

These producs apply to where:

- 1) Input voltage rang: +- 10%Vin; 2) 1,5KVDC input and output isolation;
- 3) Regulated and low ripple noise is not required

Such as: digital circuits, low frequency analog circuits, and relay drive circuit.

SELECTION G	UIDE								
Model	Input Voltage(VDC)	Output Voltage	Output (Current (mA)		t Current A)(Typ.)	Reflected Ripple	Max. Capacitive Load(µF)	Efficiency (%, Typ.)
	Nominal (Range)	(VDC)	Max.	Min.	@Max. Load	@No Load	Current (mA,Typ.)		@Max. Load
DBZ-0303-S/D1P	3.3	3.3	303	30	405	25			75
DBZ-0305-S/D1P	(2.97-3.63)	5	200	20	380	25			80
DBZ-0503-S/D1P		3.3	303	30	263				76
DBZ-0505-S/D1P		5	200	20	250			220	80
DBZ-0509-S/D1P	5	9	111	12	250	20			80
DBZ-0512-S/D1P	(4.5-5.5)	12	84	9	248	20			81
DBZ-0515-S/D1P		15	67	7	248		15		81
DBZ-0515-S/D1P		24	42	4	248				81
DBZ-1203-S/D1P		3.3	303	30	111	15			76
DBZ-1205-S/D1P		5	200	20	104				80
DBZ-1209-S/D1P	12 (10.8-13.2)	9	111	12	104				80
DBZ-1212-S/D1P	((((((((((((((((((((12	83	9	103				81
DBZ-1215-S/D1P		15	67	7	103				80
DBZ-1515-S/D1P	15 (13.5-16.5)	15	67	7	82	10			81
DBZ-2403-S/D1P		3.3	303	30	55				76
DBZ-2405-S/D1P		5	200	20	52				80
DBZ-2409-S/D1P	24	9	111	12	52	7			80
DBZ-2412-S/D1P	(21.6-26.4)	12	84	9	50	'			81
DBZ-2415-S/D1P		15	67	7	50				82
DBZ-2424-S/D1P		24	42	4	50				82

INPUT SPECIFICATIONS								
Item	Test Conditions	Max.	Unit					
Input Surge Voltage (1sec. max.)	3.3VDC Input Models	-0.7		5				
	5VDC Input Models	-0.7		9				
	12VDC Input Models	-0.7		18	VDC			
(**************************************	15VDC Input Models	-0.7		21				
	24VDC Input Models	-0.7		30				
Input Filter			Capacitor					

The information and specifications contained in this data sheet are believed to be correct at time of publication. However, ZimTec Electronics accepts no responsibility for consequences arising from printing errors or inaccuracies. Specifications are subject to change without notice. No rights under any patent accompany the sale of any such product(s) or information contained herein.



OUTPUT SPECIFICAT	IONS						
Item	Test Conditions	Test Conditions			Max.	Unit	
Output Voltage Accuracy				See tolerance	envelope curve		
Line Voltage Regulation	For Vin abongo of 110	3.3V output			±1.5	%	
	For Vin change of ±1%	Others	-		±1.2		
		3.3V output		18			
		5V output		12			
Load Regulation	10% to 100% load	9V output		8			
Load Negulation	10 % to 100 % load	12V output		7			
		15V output		6			
		24V output		5			
Temperature drift coefficient	100% load				±0.03	%/°C	
Ripple & Noise*	20MHz C	utput Voltage ≤12V		30			
	Bandwidth	thers		60		mVp-p	
Short Circuit Protection			Continuous, automatic recovery				
Note:* Ripple and noise tested with	"parallel cable" method. See	detailed operation instruction	ns at DC-DC Applicat	tion Notes.			

Note:*	Ripple and noise	tested with "par	allel cable" method	. See detailed operation	instructions at DC-DC Application Notes.	

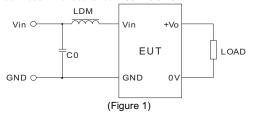
COMMON SPECIFICATIONS								
Item	Test Conditions	Min.	Тур.	Max.	Unit			
Isolation Voltage	Input-Output, Tested for 1 minute and leakage current less than 1 mA	1500			VDC			
Isolation Resistance	Input-Output, Test at 500VDC	1000			ΜΩ			
Isolation Capacitance	Input-Output,100KHz/0.1V		20		pF			
Switching Frequency	Full load, nominal input		100	300	KHz			
MTBF MIL-HDBK-217F@25℃		3500			K hours			
Case Material	Case Material		Plastic(l	JL94-V0)	-			
Weight	DBZ_S1P Series		1.2					
	DBZ_D1P Series		1.8		g			

ENVIRONMENTAL SPECIFICATIONS								
Item	Test Conditions	Min.	Тур.	Max.	Unit			
Storage Humidity	Non condensing			95	%			
Operating Temperature	Power derating (above 85°C see Figure 2)	-40		105				
Storage Temperature		-55		125	°c			
Temperature rise	Ta=25°C,100% Load		25					
Lead Temperature	1.5mm from case for 10 seconds		-	300				
Cooling		Free air convection						

EMC SPECIFICATIONS					
ЕМІ	CE	CISPR22/EN55022 CLASS B(Recommended Circuit Refer to Figure1)			
	RE	CISPR22/EN55022 CLASS B(Recommended Circuit Refer to Figure1)			
EMS	ESD	IEC/EN61000-4-2 Contact ±8KV perf. Criteria B			

EMC RECOMMENDED CIRCUIT

EMI Typical Recommended Circuit (CLASS B):



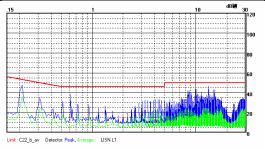
Recommended typical circuit parameters:

\	/in(V)	3.3/5/12/15/24
ЕМІ	C0	4.7µF /50V
	LDM	6.8µH

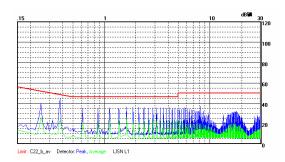
The models listed above is just for standard type. If you need the special specification product, please contact our service member by telephone presented in shortform cover or e-mail to:info@zimtec-electronics.de



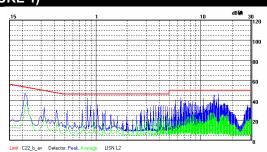
EMI TEST WAVEFORM (RECOMMENDED CIRCUIT FINGURE 1)



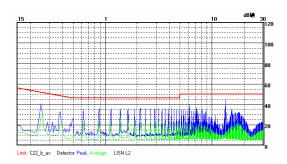
DBZ-0505-S1P CE (Class B, Positive line)



DBZ-0512-D1P CE (Class B, Positive line)

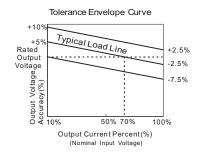


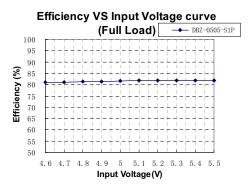
DBZ-0505-S1P CE (Class B, Negative line)

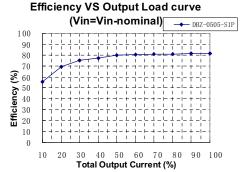


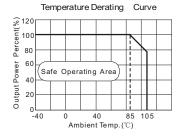
DBZ-0512-D1P CE (Class B, Negative line)

PRODUCT TYPICAL CURVE

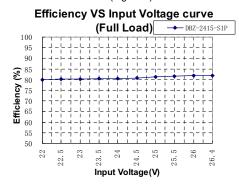


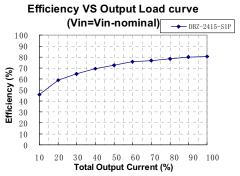






(Figure 2)



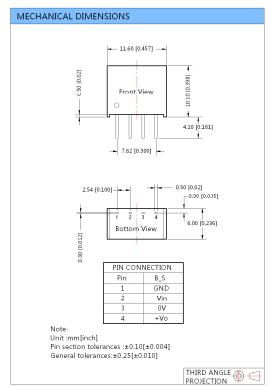


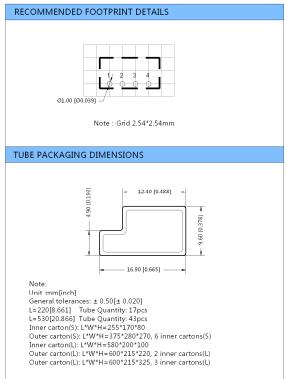
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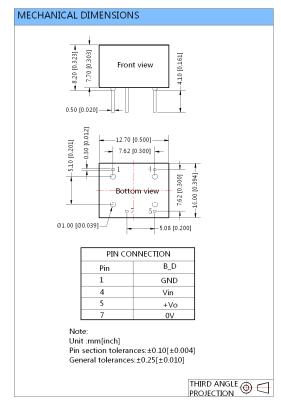
DIMENSIONS, RECOMMENDED FOOTPRINT & PACKAGING

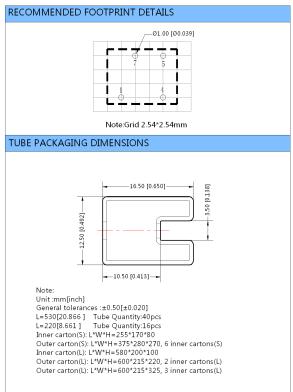
DBZ_S1P





DBZ D1P





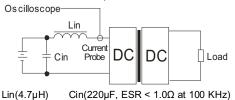
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TEST CONFIGURATIONS

Input Reflected-Ripple Current Test Setup

Input reflected-ripple current is measured with an inductor Lin and Capacitor Cin to simulate source impedance.



DESIGN CONSIDERATIONS

1) Requirement for output load

To ensure this module can operate efficiently and reliably, the minimum output load could not be less than 10% of the full load. If the actual output power is very small, please connect a resistor to the output in parallel to increase the load.

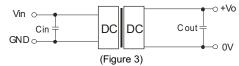
2) Overload Protection

Under normal operating conditions, the output circuit of these products have not overload protection. The simplest method is to add a breaker circuit in the circuit.

3) Recommended circuit

If you want to further decrease the input/output ripple, an capacitor filtering network may be connected to the input and output ends of the DC/DC converter, refer to Figure 3.

It should also be noted that the capacitance of the capacitor must be proper. If the capacitance is too large, a startup problem might arise. For ensuring every channel of output can provide a safe and reliable operation, the recommended capacitance of the capacitor refer to Table 1.



EXTERNAL CAPACITOR TABLE (Table 1)

Vin	Cin	Vo	Cout
(VDC)	(µF)	(VDC)	(µF)
3.3/5	4.7	3.3/5	10
12	2.2	9	4.7
15	1	12	2.2
24	1	15	1
		24	0.47

It's not recommended to connect any external capacitor in the application field with less than 0.5 watt output.

- 4) The input and the output of the product are recommended to be connected to ceramic capacitor or electrolytic capacitor. Using tantalum capacitor may cause risk of failure
- 5) It is not recommended to increase the output power capability by connecting two or more converters in parallel. The product is not hot-swappable

Note:

- 1. Operation under minimum load will not damage the converter; However, they may not meet all specifications.
- 2. Max. Capacitive Load is tested at nominal input voltage and full load.
- 3. Unless otherwise noted, All specifications are measured at Ta=25°C, humidity<75%, nominal input voltage and rated output load.
- 4. In this datasheet, all test methods are based on our corporate standards.
- 5. All characteristics are for listed models, and non-standard models may perform differently. Please contact our technical support for more detail.
- 6. Please contact our technical support for any specific requirement.
- 7. Specifications of this product are subject to changes without prior notice.

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